

IBM Soft- und Hardware Updates und News

Presented by : André Spahni
Walter Kläy

Agenda

Allgemeine News IBM	15'	André und Walter
IBM Z CyberVault	50'	Walter Kläy
IBM Quantum Computing	50'	Nico Einsidler



Stack IBM DACH

zInfo CH (Feedback)

zInfo CH 20211015

Topic	Language	Date / Time (local Swiss time)	Comment	Reg. Link
IDUG Db2 Tech Conference in Edinburgh, Scotland	English	17. - 21. October 2021	onsite	Link
Cyber resiliency to protect your business-critical infrastructure	English	19. October 2021 18:00 - 19:00	Webinar	Link
WW IBM Z Security Conference	English	19. - 22. October 2021	Webinar / (onsite)	Link
93. GSE z/OS zExpertenforum (Schweiz)	Deutsch	19. + 20. October 2021	geplant in Viznau	Link
Frühstücken mit IBM Z Stack: Nutzen und Anwendungsfälle von Deep Learning auf IBM Z mithilfe von Watson Machine Learning for z/OS	Deutsch	20. October 2021 08:30 - 09:30	Webinar	Link
Event Information: Client Webinar - IBM Cloud Infrastructure Center highlights and whats new with 1.1.4	English	20. October 2021 17:00 - 18:00	Webinar	Link
Drive greater business value from mainframe applications with deep learning AI models	English	26. October 2021 17:00 - 18:15	Webinar	Link
IBM TechU 2021 virtual edition	English	25. - 28. October 2021	Webinar	Link
Db2 AI for z/OS – Real Time Dashboards and More!	English	2. November 2021 16:00 - 17:00	Webinar	Link
Enterprise IT Tage	Deutsch	10. - 11. November 2021	Ehningen (DE)	
GSE Db2 Arbeitsgruppe Meeting (Schweiz)	Deutsch	18. November 2021	geplant bei IBM Altstetten	

An actual list of the events can be found here:

<http://ibm.biz/z-dach-community-events>

IBM Z DACH Community



IBM Z and LinuxONE Community Participate ▾ Topic groups ▾ User groups Solutions ▾ Resources

IBM Z DACH

User group for all System Z customers in Germany, Austria and Switzerland (DACH)

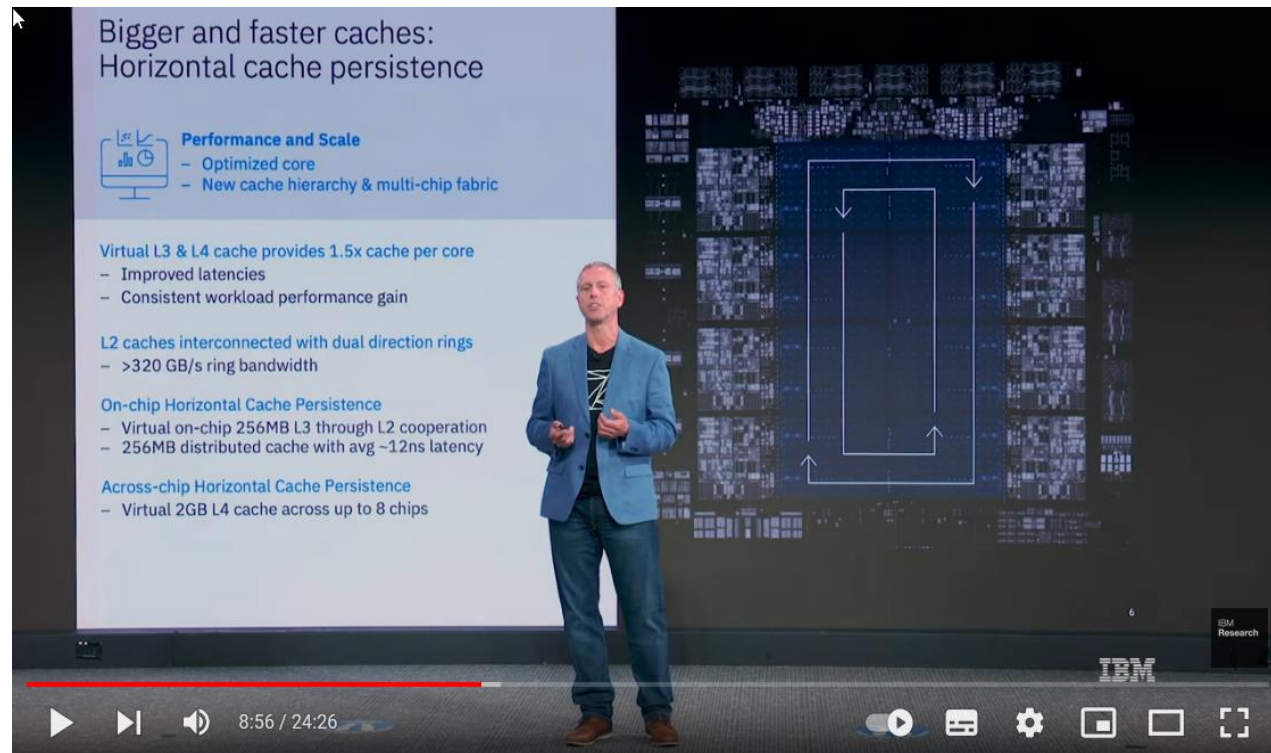
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IBM Telum Prozessor

YouTube Preview on IBM Telum Processor



Bigger and faster caches:
Horizontal cache persistence

Performance and Scale

- Optimized core
- New cache hierarchy & multi-chip fabric

Virtual L3 & L4 cache provides 1.5x cache per core

- Improved latencies
- Consistent workload performance gain

L2 caches interconnected with dual direction rings

- >320 GB/s ring bandwidth

On-chip Horizontal Cache Persistence

- Virtual on-chip 256MB L3 through L2 cooperation
- 256MB distributed cache with avg ~12ns latency

Across-chip Horizontal Cache Persistence

- Virtual 2GB L4 cache across up to 8 chips

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IBM Research

8:56 / 24:26

IBM

[IBM Z Redbooks](#)

Auswahl 2020/2021:

- [Red Hat OpenShift on IBM Z Installation Guide](#)
- [Introducing IBM Z System Recovery Boost](#)
- [IBM Z Connectivity Handbook](#)
- [Accelerating Digital Transformation on Z Using Data Virtualization](#)
- [Getting Started: Journey to Modernization with IBM Z](#)
- [IBM z15 Technical Introduction](#)
- [Getting Started with z/OS Data Set Encryption](#)
- [IBM z/OS Container Extensions \(zCX\) Use Cases](#)
- [Getting started with z/OS Container Extensions and Docker](#)
- [Demystifying Data with AI on IBM Z](#)
- [Getting Started with IBM Z Cyber Vault](#)

Be aware: Redbooks erfahren auch updates